

FIGURE 10 — VOLTAGE MONITOR

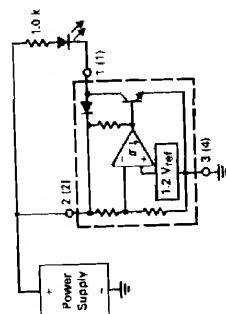


FIGURE 11 — SOLAR POWERED BATTERY CHARGER

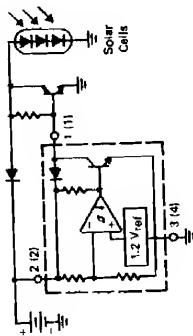


FIGURE 12 — LOW POWER SWITCHING REGULATOR

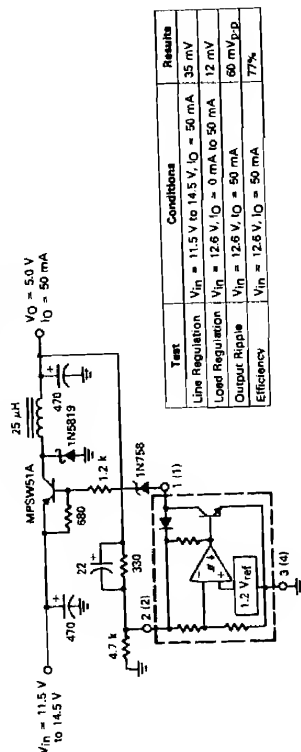
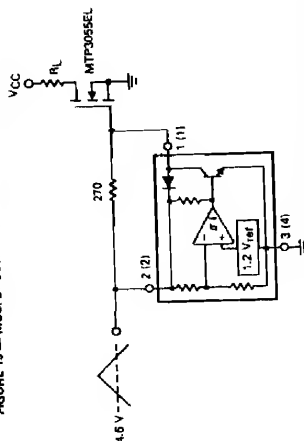


FIGURE 13 — MOSFET LOW-VOLTAGE GATE DRIVE PROTECTION



Overheating of the logic level power MOSFET due to insufficient gate voltage can be prevented with the above circuit. When the input signal is below the 4.6 volt threshold of the MC34064, its output grounds the gate of the L<sup>2</sup> MOSFET.



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## Advance Information

## HIGH PERFORMANCE DUAL CHANNEL CURRENT MODE CONTROLLER

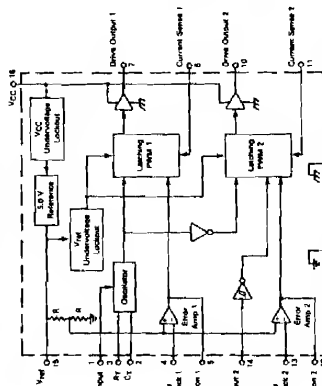
The MC34065 series are high performance, fixed frequency, dual current mode controllers. They are specifically designed for Off-Line and DC to DC converter applications offering the designer a cost effective solution with minimal external components. These integrated circuits feature a unique oscillator for precise duty cycle limit and frequency control, a temperature compensated reference, two high gain error amplifiers, two current sensing comparators, drive output 2 enable pin, and two high current totem pole outputs ideally suited for driving power MOSFETs.

Also included are protective features consisting of input and reference undervoltage lockouts each with hysteresis, cycle-by-cycle current limiting, and a latch for single pulse metering of each output.

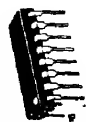
These devices are available in dual-in-line and surface mount packages.

- Unique Oscillator for Precise Duty Cycle Limit and Frequency Control
- Current Mode Operation to 500 kHz
- Automatic Feed Forward Compensation
- Separate Latching PWMs for Cycle-By-Cycle Current Limiting
- Internally Trimmed Reference with Undervoltage Lockout
- Drive Output 2 Enable Pin
- Two High Current Totem Pole Outputs
- Input Undervoltage Lockout with Hysteresis
- Low Start-Up and Operating Current
- Direct Interface with Motorola SENSEFET Products

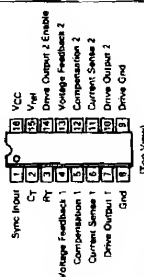
## SIMPLIFIED BLOCK DIAGRAM



This document contains information on a new product. Specifications and information herein are subject to change without notice.

MC34065  
MC33065HIGH PERFORMANCE  
DUAL CHANNEL  
CURRENT MODE CONTROLLER  
SILICON MONOLITHIC  
INTEGRATED CIRCUITP-SUFFIX  
PLASTIC PACKAGE  
CASE 648-08DW SUFFIX  
PLASTIC PACKAGE  
CASE 751G-01  
SO-16

## PIN CONNECTIONS



## ORDERING INFORMATION

Device	Temperature Range	Package
MC34065DW	0 to +70°C	SO-16
MC34065P	0 to +70°C	Plastic DIP
MC33065DW	-40 to +85°C	SO-16
MC33065P	-40 to +85°C	Plastic DIP

FIGURE 1 — RESET OUTPUT VOLTAGE versus INPUT VOLTAGE

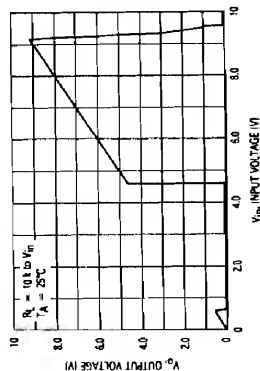


FIGURE 2 — RESET OUTPUT VOLTAGE versus INPUT VOLTAGE

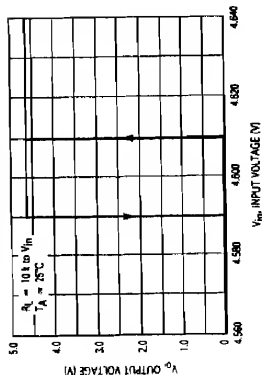


FIGURE 3 — COMPARATOR THRESHOLD VOLTAGE versus TEMPERATURE

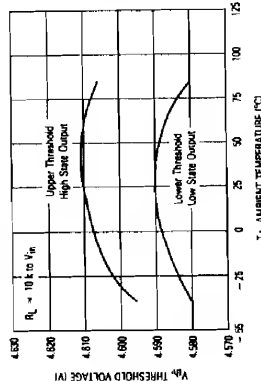


FIGURE 4 — INPUT CURRENT versus INPUT VOLTAGE

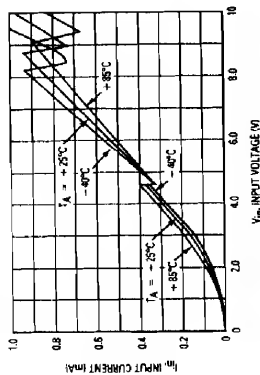


FIGURE 5 — RESET OUTPUT SATURATION versus SINK CURRENT

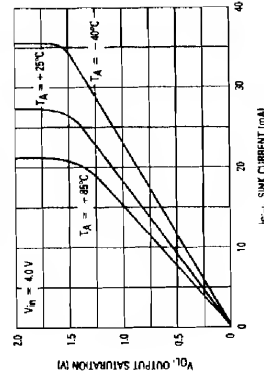


FIGURE 6 — RESET DELAY TIME

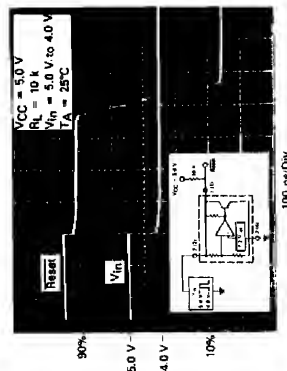


FIGURE 7 — CLAMP DIODE FORWARD CURRENT versus VOLTAGE

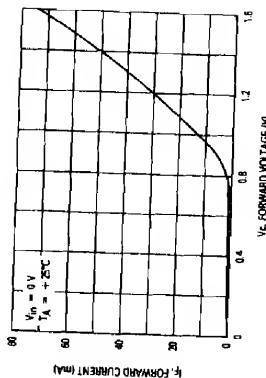


FIGURE 8 — LOW VOLTAGE MICROPROCESSOR RESET

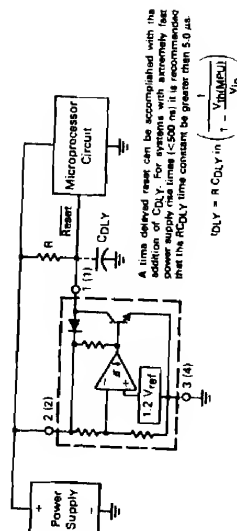
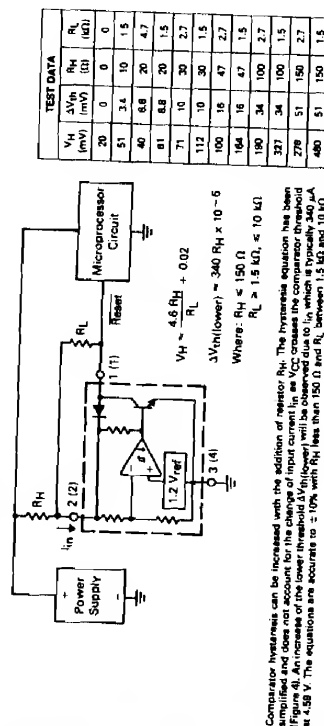


FIGURE 9 — LOW VOLTAGE MICROPROCESSOR RESET WITH ADDITIONAL HYSTERESIS





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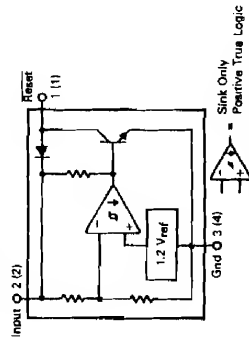
## Advance Information

### UNDervOLTAGE SENSING CIRCUIT

The MC34064 is an undervoltage sensing circuit specifically designed for use as a reset controller in microprocessor-based systems. It offers the designer an economical solution for low voltage detection with a single external resistor. The MC34064 features a trimmed-in-package bandgap reference, and a comparator with precise thresholds and built-in hysteresis to prevent erratic reset operation. The open collector reset output is capable of sinking in excess of 10 mA, and operation is guaranteed down to 1.0 volt input with low standby current. These devices are packaged in 3-pin TO-226AA and 8-pin surface mount packages. Applications include direct monitoring of the 5.0 volt MPU logic power supply used in appliance, automotive, consumer and industrial equipment.

- Trimmed-in-Package Temperature Compensated Reference
- Comparator Threshold of 4.6 V at 25°C
- Precise Comparator Thresholds Guaranteed Over Temperature
- Comparator Hysteresis Prevents Erratic Reset
- Reset Output Capable of Sinking in Excess of 10 mA
- Internal Clamp Diode for Discharging Delay Capacitor
- Guaranteed Reset Operation with 1.0 Volt Input
- Low Standby Current
- Economical TO-226AA and SO-8 Surface Mount Packages

### REPRESENTATIVE BLOCK DIAGRAM



Pin numbers adjacent to terminals are for the 3-pin TO-226AA package.  
Pin numbers in parentheses are for the D suffix SO-8 package.

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MC34064  
MC33064

### UNDervOLTAGE SENSING CIRCUIT SILICON MONOLITHIC INTEGRATED CIRCUIT

P SUFFIX  
PLASTIC PACKAGE  
CASE 29-44  
TO-226AA



PIN 1. RESET  
2. INPUT  
3. GROUND

D SUFFIX  
PLASTIC PACKAGE  
CASE 75-13  
SO-8



PIN 1. RESET  
2. INPUT  
3. N.C.  
4. GROUND  
5. N.C.  
6. N.C.  
7. N.C.  
8. N.C.

### ORDERING INFORMATION

Device	Temperature Range	Package
MC34064D-5	0°C to +70°C	Plastic SO-8
MC34064P-5	0°C to +70°C	Plastic TO-226AA
MC33064D-5	-40°C to +85°C	Plastic SO-8
MC33064P-5	-40°C to +85°C	Plastic TO-226AA

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Power Input Supply Voltage	$V_{in}$	-1.0 to 10	V
Reset Output Voltage	$V_O$	10	V
Reset Output Sink Current (Note 1)	$I_{Sink}$	Internally Limited	mA
Clamp Diode Forward Current, Pin 1 to 2 (Note 1)	$I_F$	100	mA
Power Dissipation and Thermal Characteristics			
P Suffix, Plastic Package	$P_D$	525	mW
Maximum Power Dissipation @ $T_A = 25^\circ\text{C}$	$R_{\theta JA}$	200	$^\circ\text{C/W}$
Thermal Resistance, Junction to Air			
D Suffix, Plastic Package	$P_D$	825	mW
Maximum Power Dissipation @ $T_A = 25^\circ\text{C}$	$R_{\theta JA}$	200	$^\circ\text{C/W}$
Thermal Resistance, Junction to Air			
Operating Junction Temperature	$T_J$	+150	$^\circ\text{C}$
Operating Ambient Temperature	$T_A$	0 to +70	$^\circ\text{C}$
MC34064		-40 to +95	$^\circ\text{C}$
MC33064			
Storage Temperature Range	$T_{Stg}$	-65 to +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS For typical values  $T_A = 25^\circ\text{C}$ , for min/max values  $T_A$  is the operating ambient temperature range that applies (Notes 2 and 3).

Characteristic	Symbol	Min	Typ	Max	Unit
COMPARATOR					
Threshold Voltage					V
High State Output ( $V_{in}$ Increasing)	$V_{IH}$	4.5	4.61	4.7	
Low State Output ( $V_{in}$ Decreasing)	$V_{IL}$	4.5	4.59	4.7	
Hysteresis	$V_H$	0.01	0.02	0.05	
RESET OUTPUT					
Output Sink Saturation					V
$V_{in} = 4.0\text{ V}$ , $I_{Sink} = 8.0\text{ mA}$	$V_{OL}$	—	0.46	1.0	
$V_{in} = 4.0\text{ V}$ , $I_{Sink} = 2.0\text{ mA}$		—	0.15	0.4	
$V_{in} = 1.0\text{ V}$ , $I_{Sink} = 0.1\text{ mA}$		—	—	0.1	
Output Sink Current ( $V_{in} = 4.0\text{ V}$ )	$I_{Sink}$	10	27	80	mA
Output Off-State Leakage ( $V_{in} = 5.0\text{ V}$ )	$I_{OH}$	—	0.02	0.5	$\mu\text{A}$
Clamp Diode Forward Voltage, Pin 1 to 2 ( $I_F = 10\text{ mA}$ )	$V_F$	0.6	0.9	1.2	V
TOTAL DEVICE					
Operating Input Voltage Range	$V_{in}$	1.0 to 6.5	—	—	V
Quiescent Input Current ( $V_{in} = 5.0\text{ V}$ )	$I_{in}$	—	390	500	$\mu\text{A}$

### NOTES:

1. Maximum package power dissipation limits must be observed.
2. Low duty cycle pulse techniques are used during test to maintain junction temperature as close to ambient as possible.
3.  $T_{low} = 0^\circ\text{C}$  for MC34064  
 $T_{low} = -40^\circ\text{C}$  for MC33064  
 $T_{high} = +70^\circ\text{C}$  for MC34064  
 $T_{high} = +85^\circ\text{C}$  for MC33064